IPSPR119

New feature: Added the possibility to deselect some boot devices in the Bios Setup.

IPSPR118

New feature: Added support for new version of S-ATA controller (JMB362C). The reason is a new PCI Device ID.

IPSPR117

Bug fix: Some boards don't support correctly the BASIC (03001-0000-xx-1) and STANDARD (03001-0000-xx-2) variant; many needless Bios Setup entries are shown and some needless PCI devices are enabled.

IPSPR116

New feature: ACPI mode S4 (Suspend to Disk) implemented - especially for Windows 7.

IPSPR115

Bug fix: Implemented new Poulsbo-microcode. Optimized support for several RAM modules.
Bug fix: Some boards had a problem with the ACPI mode S3 (Suspend-to-RAM) after a cold boot. This bug is fixed.

IPSPR114

Bug fix: If the power supply was switched off after a Bios update and then switched off again when the message ' Press F1 to run SETUP / Press F2 to load default values and continue' is displayed the board stopped running after the power supply was switched on. This bug is fixed.

New feature: The drive strength of the SDIO-slot (only connector J3201) is set to the maximum value (caused by different hardware stuffing option).

IPSPR113

Bug fix: The ACPI mode S3 (Suspend-to-RAM) causes a system crash at the entry point of S3 after a cold start. This bug is fixed.
Bug fix: When switching to ACPI mode S3 after change of the RAM module (different size and organisation) the system crashes. This bug is fixed.
Bug fix: For old CF-cards the DMA mode can be displayed wrong within the Bios Setup if these cards only work with asynchronous DMA access (no UDMA). This bug is fixed.

New feature: The Gate A20 can be activated through Port 92h and Bios Setup (requirement: CPLD revision 0Dh).
New feature: Implemented ACPI temperature and fan control (active/passive cooling).
New feature: Most linux distributions expect a keyboard controller at reboot. As the pITX-SP does not have a keyboard controller this can lead to problems. A Bios Setup option allows it that the port 64h accesses are intercepted so that the reboot works fine on most linux distributions.
New feature: Realized native mode for the S-ATA interface. Windows operating systems can be installed without the need of special S-ATA drivers.
New feature: Implemented the ACPI function 'Wake On USB' (requirement: CPLD revision 0Fh).
New feature: The ACPI function 'Wake On LAN' can be switched on and off (requirement: CPLD revision 10h).

New feature: The digital Input/Output interface was extended substantially. From PCB revision V5.00 (revision CE 1.0.0) all 8 I/O lines can either be defined as input or output. Additionally there is the possibility to process interrupts and to change to tri-state mode (requirement: CPLD revision 11h).
New feature: Implemented the Bios crisis recovery with USB floppy or USB key (formatted as floppy).
New feature: Removed the menu items PAID and FPID out of the LVDS part of the Bios Setup as these features are not supported anymore.
New feature: Added DTS temperature within the Bios Setup.
New feature: Added Autostart feature in S5 (soft-off) -mode (requirement: CPLD revision 11h). Either the boards performs a restart or it remains in the soft-off state (Bios Setup settings).
New feature: Revised DMI entries (Desktop Management Interface).
New feature: Implemented support for Bios update tool BF.EXE.
New feature: Optimized the ACPI device description within the CPU and SMBus part.

Attention: *All Bios revisions from this release on (R113 or later) may not be used with board revisions prior to PCB V5.00 (revision CE 1.0.0).*

IPSPR112

New feature: Due to a change within the CPLD code the digital Input/Output interface must be activated within the System Bios.

IPSPR111

New feature: The USB 2.0 ports can be switched on and off separately as the USB 1.1 ports (one USB controller includes two USB ports).

IPSPR110

First mass production Bios.