



Errata

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# Manual: 16596

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## Manual Index: 0310

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## 1. Reference: Page 1-6, Chapter 1.5, Figure 1.2

The referenced information (figure) is revised as follows.

SMC1 **is changed to read:** SMC2 (TERM port)

## 2. Reference: Page 1-10, Chapter 1.7, Text

The referenced information (text) is revised as follows.

**The text:**

(please consult for latest order information your local PEP representative)

**is changed to read:**

For the latest product ordering information, please contact your nearest Kontron Modular Computers GmbH sales office.

## 3. Reference: Page 2-9, Chapter 2.5.1.1, Table 2.5

The referenced information (table) is revised as follows.

**The table row:**

1F-18	User Defined
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**is superseded by the following table rows:**

19	A24 User Defined 2, Supervisory program access
1A	A24 User Defined 2, Supervisory data access
1D	A24 User Defined 2, non-privileged program access
1E	A24 User Defined 2, non-privileged data access

**The table row:**

17 - 10	User Defined
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**is superseded by the following table rows:**

11	A24 User Defined 1, Supervisory program access
12	A24 User Defined 1, Supervisory data access
15	A24 User Defined 1, non-privileged program access
16	A24 User Defined 1, non-privileged data access



#### 4. Reference: Page 2-20, Chapter 2.8, Figure 2.2

The referenced information (figure) is revised as follows.

SMC1 is changed to read: SMC2

#### 5. Reference: Page 2-21, Chapter 2.8, Table 2.9

The referenced information (table) is revised as follows.

SMC1 is changed to read: SMC2

#### 6. Reference: Page 2-22, Chapter 2.8.2

The referenced information (chapter) is revised as follows.

**The following figure is added.**

RJ45 Connector



**The following table is added.**

Pinout of RJ45 Connector – SC-Cloop Piggyback

Pin	Signal
1	N/C
2	N/C
3	GND
4	TxD
5	RxD
6	N/C
7	N/C
8	N/C



**The following table is added.**

Pinout of RJ45 Connector – SC-485F/I Piggyback

Pin	Signal
1	NC
2	NC
3	GND
4	+TxD
5	N/C
6	N/C
7	-TxD
8	N/C

**The text:**

*Mini D-Sub Female Connector*

**is changed to read:**

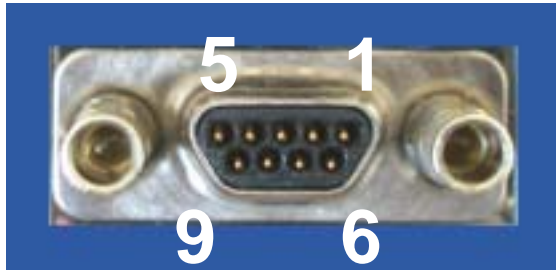
*Mini D-Sub Male Connector.*





The following figure is added.

Mini D-Sub Male Connector



The following table is added.

Pinout of Mini D-Sub Male Connector – SC232CCM

Pin	Signal
1	GND
2	DTR
3	TxD
4	RxD
5	DCD
6	N/C
7	CTS
8	RTS
9	DSR

The following table is added.

Pinout of Mini D-Sub Male Connector – SC458F/I

Pin	Signal
1	GND
2	N/C
3	+TxD
4	N/C
5	N/C
6	N/C
7	-TxD
8	N/C
9	N/C



## 7. Reference: Page 3-4, Chapter 3.2, Figure 3.1

The referenced information (figure) is revised as follows.

Jumper J1 **is changed to read:** J2

Jumper J2 **is changed to read:** J1

**The following components (DIL EPROM) are added:**

IC19

IC2

For the positioning of the EPROM sockets, IC19 and IC2, refer to the board layout at the end of the manual.

IC19: Upper Data (even) EPROM

IC2: Lower Data (odd) EPROM

**The following note is added.**



**Note ...**

Flashes cannot be used with the DIL EPROM sockets.



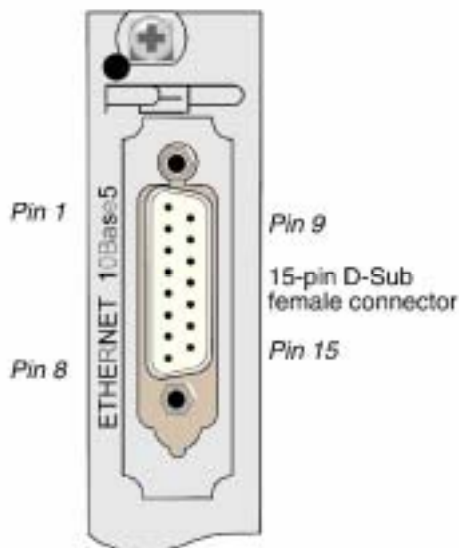
## 8. Reference: Page 3-5, Chapter 3.2.1, Table

The referenced information (table) is superseded by the following replacement table:

Jumper J1	Jumper J2	Description
Open	Open	Boot from Flash (0x04000000)
Set	Open	Boot from Eprom (0x40000000)
Open	Set	(default) Boot from VME (0x87000000)

## 9. Reference: Page SI6-3, Appendix SI6, Chapter 2.2, Figure

The referenced information (figure) is superseded by the following replacement figure.



## 10. Reference: Appendix Bootstrap Loader

The referenced information (appendix) is revised as follows:

This appendix is deleted. Please refer to Kontron Modular Computers manual:

**“Bootstrap Loader, manual ID: 16878, index 0110”**

for information concerning this subject.