



*Errata***1**

Manual: 26799

Manual Index: 03

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1. Reference: Page 1 - 13, Chapter 1.6, Table 1-2

The referenced information (table) is revised as follows.

The table row:

LEDs	<p>System status:</p> <ul style="list-style-type: none"> • TH/GP: green: overtemperature status or general purpose; when remains lit during bootup, it indicates a PCI reset is active. • WD/GP: green: Watchdog or general purpose; when remains lit during bootup, it indicates a power failure. <p>Gigabit Ethernet status:</p> <ul style="list-style-type: none"> • ACT: green: network activity • SPEED: green/orange: network speed <p>Fast Ethernet status:</p> <ul style="list-style-type: none"> • ACT: green: network activity • SPEED: green: network speed
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is changed to read:

LEDs	<p>System status:</p> <ul style="list-style-type: none"> • WD/GP: green: Watchdog or General Purpose; when remains lit during bootup, it indicates a PCI reset is active • TH/GP: green: Overtemperature Status or General Purpose; when remains lit during bootup, it indicates a power failure. <p>Gigabit Ethernet status:</p> <ul style="list-style-type: none"> • ACT: green: network activity • SPEED: green/orange: network speed <p>Fast Ethernet status:</p> <ul style="list-style-type: none"> • ACT: green: network activity • SPEED: green: network speed
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2. Reference: Page 2 - 12, Chapter 2.3.1, Text

The referenced information (text) is revised as follows.

The text:

Additionally, if the TH LED remains on during bootup, it indicates a PCI reset is active, and if the WD LED remains on during bootup, it indicates a power failure.

is changed to read:

Additionally, if the WD LED remains on during bootup, it indicates a PCI reset is active, and if the TH LED remains on during bootup, it indicates a power failure.

3. Reference: Page 2 - 25, Chapter 2.3.8.1, Table 2-19

The referenced information (table) is superseded by the following table.

Table 2-19: Rear I/O CompactPCI Bus Connector J2 Pinout

PIN	Z	A	B	C	D	E	F
22	GND	NC	NC	NC	NC	NC	GND
21	GND	CLK6	GND	TDN1	RDN1	RDP1	GND
20	GND	CLK5	GND	TDP1	GND	RIO_VCC	GND
19	GND	GND	GND	RES	RES	RIO_+3.3V	GND
18	GND	RES	UV2-	UV4+	RTC Bat	RIO_+3.3V	GND
17	GND	RES	ROUT (GND)	PRST#	REQ6#	GNT6#	GND
16	GND	RES	UV2+	DEG#	GND	UV4-	GND
15	GND	RES	GOUT (GND)	FAL#	REQ5#	GNT5#	GND
14	GND	2RIN	2DSR	2RTS	VSYNC (GND)	2CTS	GND
13	GND	2RXD	FANSENSE (GND)	BOUT (VIO)	2DTR	2DCD	GND
12	GND	1DSR	1RTS	1CTS	HSYNC (GND)	2TXD	GND
11	GND	1DTR	BOUT (GND)	IDE_PD[9]	1DCD	1RIN	GND
10	GND	IDE_PD[8]	IDE_RST#	1TXD	IDE_PD[10]	1RXD	GND
9	GND	IDE_PD[6]	IDE_PD[7]	IDE_PD[4]	IDE_PD[5]	IDE_PD[11]	GND
8	GND	IDE_PD[3]	IDE_PD[12]	IDE_PD[2]	IDE_PDIAG#/CBLID#	IDE_PD[1]	GND
7	GND	IDE_PD[14]	IDE_PD[0]	IDE_PD[15]	IDE_PDRO#	IDE_PIO#	GND
6	GND	IDE_PIOR	IDE_PIORDY	IDE_PDACK#	IDE_PD [13]	IDE_PIRQ14	GND
5	GND	IDE_PA[1]	IDE_PDASP#	IDE_PA[0]	IDE_PA[2]	TH_GP/ SLP_S3	GND
4	GND	VIO	RIO_VCC	IDE_PCS0#	GND	IDE_PCS1#	GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	SYSEN#	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND

The following signals have been changed:

- B5: from GND to IDE_PDASP#
- D8: from GND to IDE_PDIAG#/CBLID#
- C4: from IDE_PCS1# to IDE_PCS0#
- E4: from IDE_PCS3# to IDE_PCS1#

4. Reference: Page 4 - 10, Chapter 4.5.5, Table 4-10

The referenced information (table) is superseded by the following table.

Table 4-10: I/O Status Register

REGISTER NAME		I/O Status Register						ACCESS			
ADDRESS		0x286						R			
BIT POSITION		MSB	7	6	5	4	3	2	1	0	LSB
CONTENT		WST	Res.	Res.	Res.	CSLOT	CENUM	CFAIL	CDER		
DEFAULT		1	0	0	0	0	1	1	1		
BIT	NAME	VAL	DESCRIPTION								
0	CDER	0	Indicates power derating (CPCI DEG signal)								
		1	Power normal								
1	CFAIL	0	Indicates a power supply failure (CPCI FAIL signal)								
		1	Power normal								
2	CENUM	0	Indicates the insertion or removal of a hot swap system board (CPCI ENUM)								
		1	No hot swap event								
3	CSLOT	0	Indicates that the board is installed in a system slot								
		1	Indicates that the board is installed in a peripheral slot								
4		0	Reserved								
5		0	Reserved								
6		0	Reserved								
7	WST	0	Indicates that a Watchdog timeout has occurred								
		1	Indicates that no Watchdog timeout has occurred								

The following default settings have been changed:

- Bit position 0: from 0 to 1
- Bit position 1: from 0 to 1
- Bit position 2: from 0 to 1

5. Reference: Page 5 - 32, Chapter 5.6.3, Figure 5-18

The referenced information (figure) is superseded by the following figure.

Figure 5-18: Temperature Management - Screen Display

PhoenixBIOS Setup Utility		OEM Features
Temperature Management		Item Specific Help
Automatic Thermal Monitor	[Disabled]	
Auto Thermal Throttling:	[Enabled]	
Temperature:	[95 °C/203 °F]	
CPU Performance:	[50%]	
Processor Term Trip	[125 °C/257 °F]	
F1 Help ⚡ Select Item -/+ Change Values F9 Setup Defaults ESC Exit ↻ Select Menu Enter Select ▶ Sub-Menu F10 Save and Exit		

6. Reference: Page 5 - 32, Chapter 5.6.3, Table 5-18

The referenced information (table) is superseded by the following table.

Table 5-18: Temperature Management Options

FEATURE	OPTIONS	DESCRIPTION
Automatic Thermal Monitor	Disabled Enabled	Thermal Monitor is enabled and when the die temperature is very near to the temperature limits of the processor, the clocks will be modulated by alternately turning the clocks off and on at a duty cycle specific to the processor.
Auto Thermal Throttling	Enabled Disabled	Reduces CPU speed to avoid overheating.
Temperature	95°C – 110°C	CPU clock throttling starts when select Temperature is reached.
CPU Performance	12.5% 25% 50% 75%	The CPU performance will be reduced to the selected value when reaching the temperature threshold.
Processor Term Trip	N/A	Shows the maximum processor temperature.



7. Reference: Page 7 - 4, Chapter 7.1.1.2, Text

The referenced information (text) is revised as follows.

The text:

The thermal control circuit does not automatically go inactive once the temperature goes below the selected thermal trip point. Explicit software action is necessary to switch back to normal mode.

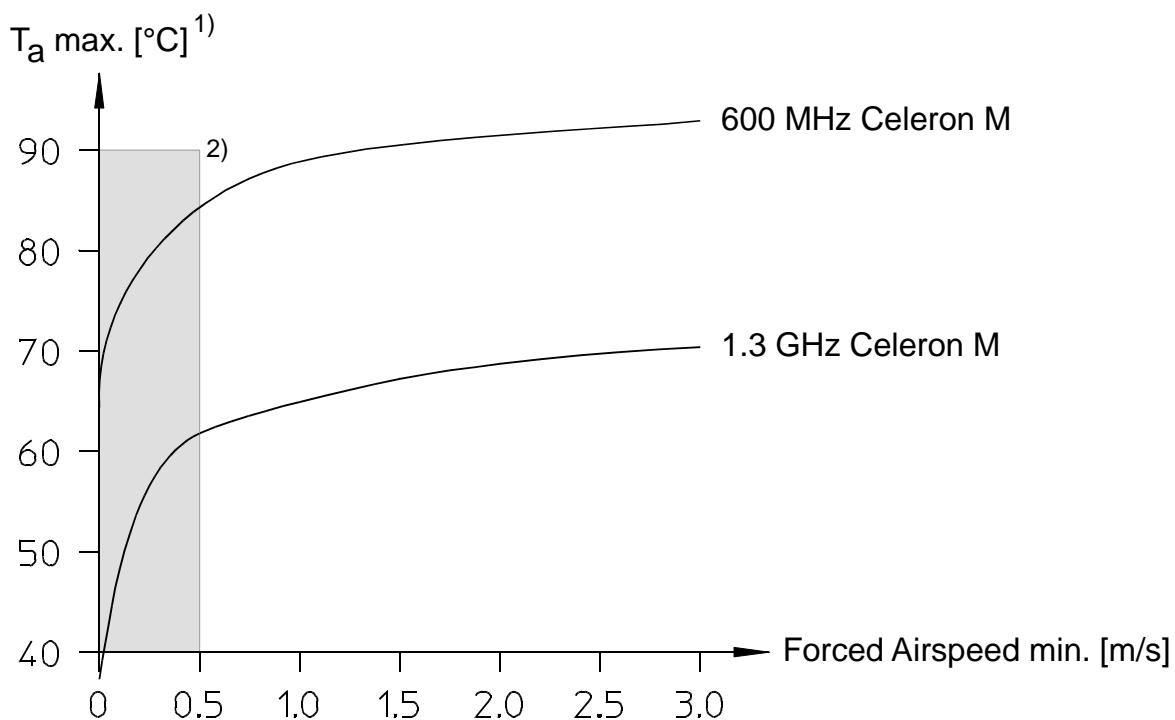
is changed to read:

The thermal control circuit automatically goes inactive once the temperature goes approximately 5 °C below the selected thermal trip point.

8. Reference: Page 7 - 7, Chapter 7.1.2.2, Figure 7-2

The referenced information (figure) is superseded by the following figure.

Figure 7-1: Mobile Celeron® M Temperature Vs. Airspeed Graph





9. Reference: Page 6 - 4, Chapter 6.4, Text

The referenced information (text) is revised as follows.

The text:

The required behavior is described in the ATX (<http://www.formfactors.org/FFDetail.asp?FFID=1&CatID=2>) and the CompactPCI (PICMG, <http://www.picmgeu.org/>) specification.

is changed to read:

For information on the required behavior refer to the power supply specifications on the formfactors.org web site and to the CompactPCI (PICMG) specification on the picmgeu.org web site.

10. Reference: Page B - 11, Chapter B.4.7, Table B-7

The referenced information (table) is superseded by the following table.

Table B-7: Rear I/O CompactPCI Bus Connector CON1 Pinout

PIN	Z	A	B	C	D	E	F
22	GND	GA4	GA3	GA2	GA1	GA0	GND
21	GND	CLK6	GND	TDN1	RDN1	RDP1	GND
20	GND	CLK5	GND	TDP1	GND	RIO_VCC	GND
19	GND	GND	GND	RES	RES	RIO_+3.3V	GND
18	GND	RES	UV2-	UV4+	RTC Bat	RIO_+3.3V	GND
17	GND	RES	ROUT (GND)	PRST#	REQ6#	GNT6#	GND
16	GND	RES	UV2+	DEG#	GND	UV4-	GND
15	GND	RES	GOUT (GND)	FAL#	REQ5#	GNT5#	GND
14	GND	2RIN	2DSR	2RTS	VSYNC (GND)	2CTS	GND
13	GND	2RXD	FANSENSE (GND)	BOUT (VIO)	2DTR	2DCD	GND
12	GND	1DSR	1RTS	1CTS	HSYNC (GND)	2TXD	GND
11	GND	1DTR	BOUT (GND)	IDE_PD[9]	1DCD	1RIN	GND
10	GND	IDE_PD[8]	IDE_RST#	1TXD	IDE_PD[10]	1RXD	GND
9	GND	IDE_PD[6]	IDE_PD[7]	IDE_PD[4]	IDE_PD[5]	IDE_PD[11]	GND
8	GND	IDE_PD[3]	IDE_PD[12]	IDE_PD[2]	IDE_PDIAG#/ CBLID#	IDE_PD[1]	GND
7	GND	IDE_PD[14]	IDE_PD[0]	IDE_PD[15]	IDE_PDRO#	IDE_PIOW#	GND
6	GND	IDE_PIOR	IDE_PIORDY	IDE_PDACK#	IDE_PD [13]	IDE_PIRQ14	GND
5	GND	IDE_PA[1]	IDE_PDASP#	IDE_PA[0]	IDE_PA[2]	TH_GP/SLP_S3	GND
4	GND	VIO	RIO_VCC	IDE_PCS0#	GND	IDE_PCS1#	GND
3	GND	CLK4	GND	GNT3#	REQ4#	GNT4#	GND
2	GND	CLK2	CLK3	SYSEN#	GNT2#	REQ3#	GND
1	GND	CLK1	GND	REQ1#	GNT1#	REQ2#	GND

The following signals have been changed:

- B5: from GND to **IDE_PDASP#**
- D8: from GND to **IDE_PDIAG#/CBLID#**
- C4: from IDE_PCS1# to IDE_PCS0#
- E4: from IDE_PCS3# to IDE_PCS1#