



---

*Errata*

---



---

*Manual: 03096, 19958*

---

*Manual Index: 0300*

**Table of Contents:**

1. Reference: Pages: 3-7, 3-8, Chapter 3.4.3, Table 3-4 ..... 1 - 3

This page was intentionally left blank.



---

**1. Reference: Pages: 3-7, 3-8, Chapter 3.4.3,  
Table 3-4**

The referenced table is superceded by the following table. This table includes the pinout of the 50-Pin, female Dsub connector. Depending on the type of carrier board in use, the 50-Pin, male on-board connector and/or the 50-Pin, female Dsub connector may not be available. The table is to be read in context with the carrier board used and in conjunction with the connector used for the AD-SIO4 Adapter.



Table 1-1: PB-SIO4A to Carrier Board to AD-SIO4 Pinout Table (Both Positions)

POS./DUART		Signal	ST102	Carrier Board		AD-SIO4 BU5	AD-SIO4 Front Panel		
P	D			MFO	DSUB		PINS	CON/CH	
U P P E R	2	-RxD D	1, 2	50	50	24	11	BU4	D
		+RxD D	25,26	49	17	23	10		
		VccE	3	48	33	22	15		
		+TxD D	4	47	49	21	8		
		-TxD D	5	46	16	20	9		
		GndE	6	45	32	19	1,7		
		-RxD C	7	44	48	18	11	BU3	C
		+RxD C	8	43	15	17	10		
		VccE	9	42	31	16	15		
		+TxD C	10	41	47	15	8		
		-TxD C	11	40	14	14	9		
		GndE	12	39	30	13	1,7		
	-RxD B	13	38	46	12	11	BU2	B	
	+RxD B	14	37	13	11	10			
	VccE	15	36	29	10	15			
	+TxD B	16	35	45	9	8			
	-TxD B	17	34	12	8	9			
	GndE	18	33	28	7	1,7			
	-RxD A	19	32	44	6	11	BU1	A	
	+RxD A	20	31	11	5	10			
	VccE	21	30	27	4	15			
	+TxD A	22	29	43	3	8			
	-TxD A	23	28	10	2	9			
	GndE	24	27	26	1	1,7			
Not Used				26	42				
Not Used				25	9				



POS./DUART		Signal	ST102	Carrier Board		AD-SIO4 BU5	AD-SIO4 Front Panel		
P	D			MFO	DSUB		PINS	CON/CH	
L O W E R	2	-RxD D	1, 2	24	25	24	11	BU4	D
		+RxD D	25,26	23	41	23	10		
		VccE	3	22	8	22	15		
		+TxD D	4	21	24	21	8		
		-TxD D	5	20	40	20	9		
		GndE	6	19	7	19	1,7		
		-RxD C	7	18	23	18	11	BU3	C
		+RxD C	8	17	39	17	10		
		VccE	9	16	6	16	15		
		+TxD C	10	15	22	15	8		
		-TxD C	11	14	38	14	9		
		GndE	12	13	5	13	1,7		
	-RxD B	13	12	21	12	11	BU2	B	
	+RxD B	14	11	37	11	10			
	VccE	15	10	4	10	15			
	+TxD B	16	9	20	9	8			
	-TxD B	17	8	36	8	9			
	GndE	18	7	3	7	1,7			
	-RxD A	19	6	19	6	11	BU1	A	
	+RxD A	20	5	35	5	10			
	VccE	21	4	2	4	15			
	+TxD A	22	3	18	3	8			
	-TxD A	23	2	34	2	9			
	GndE	24	1	1	1	1,7			

Legend: POS(ition)/DUART, P, D; Carrier Board, MFO, DSUB; CON/CH

where: P = position on carrier board  
D = DUART

MFO = 50-Pin, male flat ribbon connector, front panel or on-board

DSUB = 50-Pin, female Dsub front panel connector

CON = connector

CH = channel

This page was intentionally left blank.