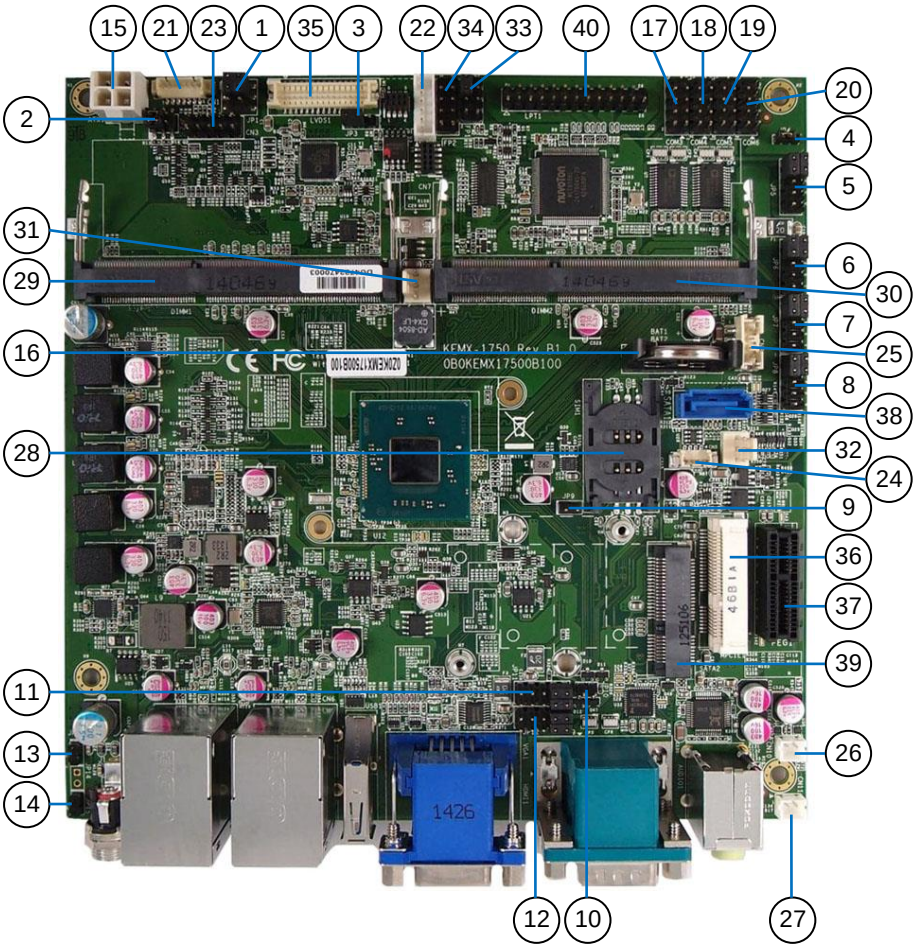
mITX-BYT - QUICK INSTALLATION GUIDE

Thank you for purchasing the Kontron mITX-BYT embedded board. This document provides information to allow you to quickly install this product.

Packing Checklist

1. Take the mITX-BYT out of the packing box and check if the unit is properly secure in the plastic bag.
2. Check the contents of the carton box: (*: optional)
   - mITX-BYT main board
   - Rear I/O Shield
   - COM Port Cable*
   - DIO Cable*
   - 1-Port USB 2.0 Cable*
   - SATA Cable*
   - SATA Power Cable*
   - LPT Cable*
   - PS/2 Cable*

Jumper and Internal Connector Locations



Jumper List

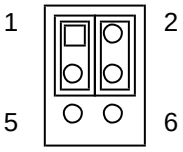
Item	Designation	Description
1	JP1	Panel & Backlight Power Selection for LVDS1
2	JP2	Backlight Power Enable Selection for LVDS1
3	JP3	AT / ATX Mode Selection
4	JP4	LVDS1 Backlight DC / PWM Selection
5	JP5	Signal / Power Selection for COM5
6	JP6	Signal / Power Selection for COM6
7	JP7	Signal / Power Selection for COM3
8	JP8	Signal / Power Selection for COM4
9	JP9	ME F/W Selection
10	JP10	MPCIE Activity LED Indication
11	JP11	Signal / Power Selection for COM2
12	JP12	Signal / Power Selection for COM1
13	JP13	USB Power Selection
14	JP15	RTC Reset Selection

Internal Connector List

Item	Designation	Description
15	ATX1	4-pin ATX Power Input Connector
16	BAT1	CR2032 Battery Holder
17	COM3	RS-232 Port 3 Pin Header
18	COM4	RS-232 Port 4 Pin Header
19	COM5	RS-232 Port 5 Pin Header
20	COM6	RS-232 Port 6 Pin Header
21	CN1	Panel Backlight Wafer for LVDS1
22	CN2	Keyboard & Mouse Wafer
23	CN3	Digital Input / Output Pin Header
24	CN5	USB2.0 Port USB DN1 Pin Header
25	CN8	HDD Power Output Wafer
26	CN10	Right Channel 3W Audio AMP Output Wafer
27	CN11	Left Channel 3W Audio AMP Output Wafer
28	SIM1	SIM Interface Wafer for MPCIE1
29	DIMM1	Primary DDR3 Memory SO-DIMM Socket
30	DIMM2	Secondary DDR3 Memory SO-DIMM Socket
31	FAN1	CPU FAN Wafer
32	FAN2	SYSTEM FAN Wafer
33	FP1	Front Panel 1 Pin Header
34	FP2	Front Panel 2 Pin Header
35	LVDS1	Primary 24-bit, 1-channel LVDS Panel Connector
36	MPCIE1	Half Size Mini-PCIE Express v1.2 Socket
37	PEG1	PCIE Express x1Slot
38	SATA1	Serial ATA Port 0 Connector
39	SATA2	Serial ATA Port-1 mSATA Socket (Full Size)
40	LPT1	Parallel Port Pin Header

Jumper Settings

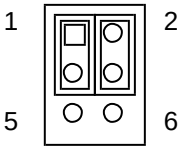
- Panel & Backlight Power Selection for LVDS1 (JP1)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Power = +12V
-	X	Backlight Power = +5V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Panel Power = +3.3V
-	X	Panel Power = +5V

"X" = Jumper set (short) and "-" = jumper not set (open)

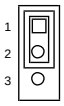
- Backlight Power Enable Selection for LVDS1 (JP2)



Jumper 1 Position		Description
Pin 1-3	Pin 3-5	
X	-	Backlight Enable Voltage = +3.3V
-	X	Backlight Enable Voltage = +5V
Jumper 2 Position		Description
Pin 2-4	Pin 4-6	
X	-	Active High
-	X	Active Low

"X" = Jumper set (short) and "-" = jumper not set (open)

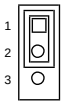
- AT / ATX Power Mode Selection (JP3)



Jumper 1 Position		Description
Pin 1-2	Pin 2-3	
X	-	ATX Mode
-	X	AT Mode

“X” = Jumper set (short) and “-” = jumper not set (open)

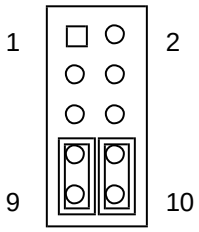
- LVDS1 Backlight DC / PWM Selection (JP4)



Jumper 1 Position		Description
Pin 1-2	Pin 2-3	
X	-	DC Mode
-	X	PWM Mode

“X” = Jumper set (short) and “-” = jumper not set (open)

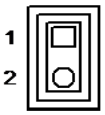
- Signal / Power Selection for COM1 ~ COM6 (JP5 ~ JP8, JP11, JP12)



Jumper 1 Position				Description
Pin 1-3	Pin 3-5	Pin 5-7	Pin 7-9	
X	-	-	-	Pin 1 = +12V
-	X	-	-	Pin 1 = +5V
-	-	X	-	Pin 1 = +5V
-	-	-	X	Pin 1 = DCD
Jumper 2 Position				Description
Pin 2-4	Pin 4-6	Pin 6-8	Pin 8-10	
X	-	-	-	Pin 9 = +12V
-	X	-	-	Pin 9 = +5V
-	-	X	-	Pin 9 = +5V
-	-	-	X	Pin 9 = RI

"X" = Jumper set (short) and "-" = jumper not set (open)

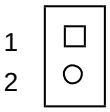
- ME F/W Selection (JP9)



Jumper Position	Description
Pin 1-2	
X	ME F/W Disabled
-	Normal Operation

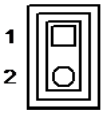
"X" = Jumper set (short) and "-" = jumper not set (open)

- MPCIE Activity LED Indication (JP10)



Pin	Signal	Note
1	LED+	
2	LED-	

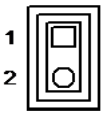
- USB Power Selection (JP13)



Jumper Position Pin 1-2	Description
X	USB power is always supply.
-	USB power will be cut off in S4 & S5 state.

“X” = Jumper set (short) and “-” = jumper not set (open)

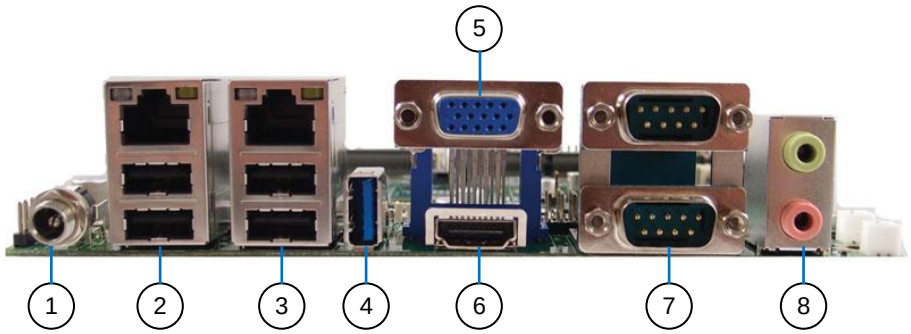
- RTC Reset Selection (JP15)



Jumper Position Pin 1-2	Description
X	Enable Clear CMOS RTC content (board does not boot with the jumper in this position)
-	Normal operation (default position)

“X” = Jumper set (short) and “-” = jumper not set (open)

Real I/O Panel Connector Locations



Item	Designation	Description
1	J1	Power Input DC Jack
2	CN4	LAN1 & USB2.0 Port 0,1 Connector
3	CN6	LAN2 & USB2.0 Port 2,3 Connector
4	USB1	USB3.0 Port 0 Connector
5	VGA1	VGA Connector
6	HDMI1	HDMI Connector
7	CN9	RS-232 / 422 / 485 Port 1, 2 Connector
8	AUDIO1	2 Stack-up Azalia Audio Phone Jack