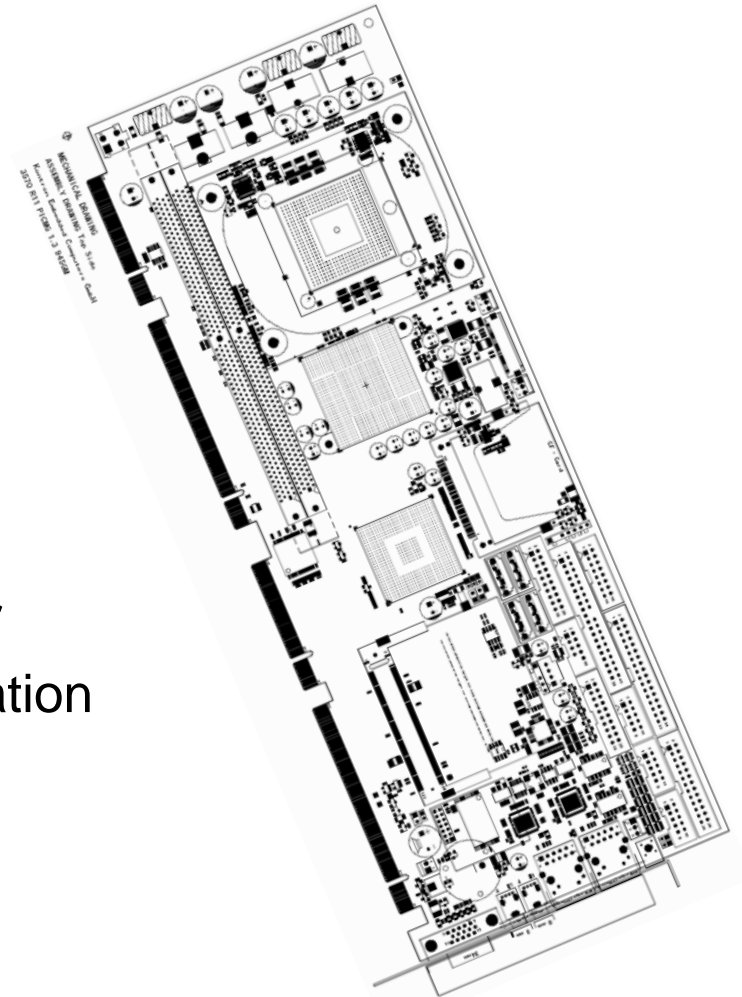


# ➤ PCI-960: Why PICMG 1.3

- Flexibility and Performance
  - PCIe, PCI, LAN, USB SATA on backplane
  - Up to date CPU support
- Upgrade for existing infrastructure
  - Existing chassis fits
  - Only SBC and Backplane update
- Wide support
  - Many existing backplanes
  - Many vendors

## ➤ PCI-960: Features

- Intel '945G + ICH7R
  - GMA 950 graphics
  - 4 Device Matrix RAID support
  - Core2Duo support
- Dual PCIe based LAN
  - Full bandwidth on both channels
  - LAN-Boot Support
- Third PCI LAN on edge connector
  - Support for inner system communication
- Standard IO features
  - 2x COM, LTP, FDD, PS/2
  - miniPCI for e. g. SCSI

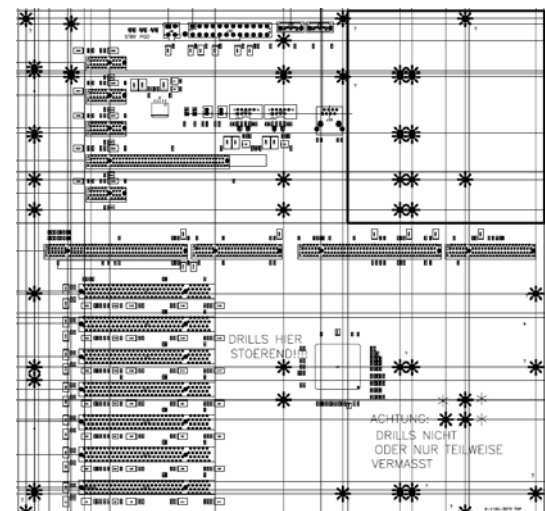
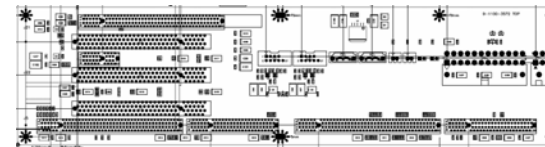


## ➤ **PCI-960: Timeframe**

- Internal prototypes
  - 3. October 2006
- Official launch
  - SPS/IPC/DRIVES 2006 (28.11.2006)
- Beta-Customer-shipment
  - Beginning December
- Serial production
  - Beginning 2007

# ➤ PCI-960: Add-ons

- 2U Backplan
  - 3x PCI 32/33
  - 1x PEG
  - 1x PCIe X1
- 4U Backplane
  - 7x PCI 32/33
  - 1x PEG
  - 4x PCIe X1



## ➤ PCI-960: KISS 2U

### ➤ KISS 2U



### ➤ Rear-IO

- 3x PCI 32/33
- 1x PEG
- 1x PCIe X1
- PCI-960 IO

